

Amendments to the Specification

- 5 Please replace paragraph [0014] with the following amended paragraph:

[0014] According to the claimed invention, an impedance matching circuit is connected between an input circuit and
10 an output circuit, the input circuit generating a target signal and an image signal associated with the target signal, the image signal being heterodyne noise or superheterodyne noise of the target signal. The impedance matching circuit includes a circuit board having a metal membrane which functions as
15 a ground layer for providing a reference ground voltage and a first, second, and third microstrip circuit. The first microstrip circuit has a first microstrip line positioned on the circuit board and coupling with the metal membrane to form a first signal-coupling structure. The first microstrip line
20 includes a first terminal connected to the input circuit and a second terminal being an open stub. The second microstrip circuit has a second microstrip line positioned on the circuit board and coupling with the metal membrane to form a second signal-coupling structure. The second microstrip line includes
25 a first terminal being open-circuited and a second terminal connected to the output circuit. The third microstrip circuit has a third microstrip line, with a third predetermined length being determined according to a frequency of the image signal, and positioned on the circuit board and coupling with the metal
30 membrane to form a third signal-guiding structure. The third microstrip line includes a first terminal connected to either the first microstrip line or the second microstrip line and

a second terminal being open-circuited. The first, second, and third microstrip lines are conductive bars. When the target signal and the image signal are both inputted into the impedance matching circuit, the image signal will bypass through the third microstrip line toward the ground layer, and the first microstrip line couples with the second microstrip line to generate an electromagnetic coupling to pass the target signal from the first microstrip line to the second microstrip line and output the target signal to the output circuit.

10